

Notice of References Cited	Application/Control No. 10/810,429	Applicant(s)/Patent Under Reexamination ALLEN ET AL.	
	Examiner Peter Coughlan	Art Unit 2129	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2003/0144746	07-2003	Hsiung et al.	700/28
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	'MOS Fully Analog Reinforcement Neural Network Chip': Al-Nsour, Abdel-Aty-Zohdy, 2001, IEEE, 0-7803-6685-9, 237-240
	V	Japan Patent Publication JP 05-187985, Fumihiro, 1992, Japan Patent Office, detailed description section
	W	'Pulsed Neural Networks': Maass, Bishop, 1999, MIT Press, xiii-xix, p17-18
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.